IN THE CLAIMS:

Please amend claims 1 and 12 as follows:

Sub B1

1. (Amended) A display controller for receiving video data from a data bus in a component YUV format and storing the video data to a display memory in a pixel video format, said display controller comprising:

a1

bus interface means, coupled to the data bus, for receiving video data in a component YUV format and corresponding video data addresses within a predetermined address range; and

a display memory controller, coupled to [said at least one memory configuration register and] said bus interface means, for receiving video data in a component YUV format <u>in contiguous successive streams of luminance and chrominance difference data</u> and corresponding video data addresses within a predetermined address range and for storing said video data <u>by directing separate luminance and chrominance difference data into predetermined memory portions according to a predetermined memory aperture so as to store said video data</u> in a pixel video format in a display memory.

Sub B2 a2 cont

12. (Amended) A method for assisting decoding of video data partially decoded in a host processor, said method comprising the steps of:

receiving, in a display controller, video data in a component YUV format in contiguous successive streams of luminance and chrominance difference data and corresponding video data addresses within a predetermined address range, and

A2 cancld

storing the video data by directing separate luminance and chrominance difference data into predetermined memory portions according to a predetermined memory aperture so as to store said video data in a pixel video format in a display memory.